

AMENDMENTS TO THE SPECIFICATION

Please delete the present second full paragraph, appearing on lines 10-15, on page 9 of the specification and replace it with the following new paragraph.

To carry out said comparison and to store indices of code vectors already processed, precalculations yielding said cross multiplication expression for each code vector c_t - according to the invention - are carried out in parallel by using specifically designed calculation ~~unit~~ units 4 of a specifically designed digital signal processor (DSP) 1 or coder/decoder 6, with configurable hardware 5. Postcalculations after performing said comparison are also performed in parallel.